

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

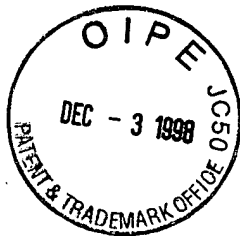
In re Patent Application of

ELIYAHOU HARARI et al.

Serial No.: 09/129,675

Filed: August 5, 1998

For: FLASH EEPROM SYSTEM



Group Art Unit: 2785

San Francisco, California

Assistant Commissioner of Patents  
Washington, D.C. 20231

**SECOND PRELIMINARY AMENDMENT AND  
REQUEST FOR INTERFERENCE**

Sir:

Please preliminarily amend the above-identified application by adding the following new claims:

B1  
-92. The device according to claim 63, wherein the array of memory cells includes the plurality of memory cells grouped into blocks of cells wherein the cells within individual blocks are resettable together to a single given threshold level range prior to the programming circuits applying said write voltages to the selective block.

93. The device according to claim 92, wherein the blocks of cells individually contain a number of spare cells, and further wherein the device comprises means for substituting the spare cells of a particular block in place of any defective cells within said particular block.

94. The device according to claim 92, additionally comprising a circuit connected with the blocks of cells to simultaneously reset the threshold levels of cells within a selected two or more blocks to said given threshold level range.

#7/ Reg Interference  
B w/ Reg Amend and  
JCSO 07/16/99  
DEC 1998  
Group 2785  
6/30/99